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INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO FORM 1449	Atty. Docket No. 10191/1639	Serial No. To Be Assigned
	Applicant(s) GLOECKLER et al.	
	Filing Date Herewith	Group To Be Assigned

10841 U.S. PTO
09/158675
01/11/01

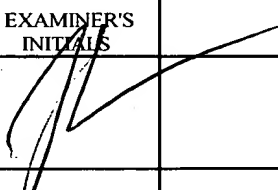
U. S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE

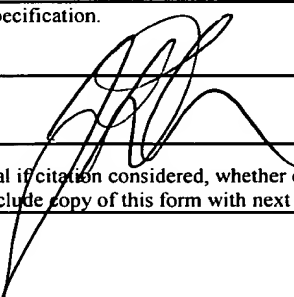
FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	SUN MICROELECTRONICS, Mountain View, CA, USA, "Introduction to JTAG Boundary-Scan", White Paper, January 1997, Part No. WPR-0018-01, and on the Internet on the web page "Analoges Boundary Scan, IEEE P1149.4"*

* Described in the Specification.

EXAMINER 	DATE CONSIDERED 6/30/2007
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	